

Patent Assignment Abstract of Title

Total Assignments: 1**Application #:** 09753254 **Filing Dt:** 12/29/2000**Patent #:** NONE**Issue Dt:****PCT #:** NONE**Publication #:** 20020129228**Pub Dt:** 09/12/2002**Inventors:** David A. Helder, Kalyan Muthukumar**Title:** Mechanism to avoid explicit prologs in software-pipelined loops**Assignment: 1**

Reel/Frame: <u>011428/0424</u>	Received: 01/18/2001	Recorded: 12/29/2000	Mailed: 03/28/2001	Pages: 3
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Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).**Assignors:** HELDER, DAVID A.**Exec Dt:** 12/29/2000MUTHUKUMAR, KALYAN**Exec Dt:** 12/29/2000**Assignee:** INTEL CORPORATION

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Web interface last modified: Oct. 5, 2002